**THIMMAPOOR ASHOK**

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**CAREER OBJECTIVE:**

As a post-graduate student and a graduate with a work experience looking for an opportunity to work for a firm with a professional work driven environment where I can utilize and apply my skills which would enable to grow while fulfilling organizational goals.

**EDUCATION:**

|  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- |
| **S.No** | **Course** | **Specialization** | **Year of Passing** | **Institution** | **Percentage/CGPA** |
| 1 | M.E | Embedded Systems and VLSI Design | 2020 (Expected) | Osmania University | 8.41(till second semester) |
| 2 | B.Tech | Electronics and Communication Engineering | 2014 | Gokaraju Rangaraju Institute of Engineering and Technology, Hyderabad. | 73.75 |
| 3 | Intermediate (BIEAP) | MPC | 2010 | Sri chaithanya Junior College, Hyderabad | 93.4 |
| 4 | 10th (SSC) | ----- | 2008 | Kakatiya High School,Nizamabad | 96 |

**TECHNICAL SKILLS:**

* **Programming/Scripting Languages :**Basics of **C**, Basics of **PERL.**
* **HDL :** Verilog
* **EDA Tools :** Xilinx Vivado, Cadence Virtuoso, Keil uvision

**WORK EXPERIENCE:**

**Organization:** Techmahindra Pvt Ltd.

**Duration:** May 2016- June 2017

**Role:** Software Engineer

**Work Description:** As a Software Engineer, I was responsible for

* support for Ahli Bank project which involved in collection and analyzing of data.
* Collect the data design the solution with team and do the corrections as per client’s requirement.

**Organization:** AM signaling Pvt Ltd.

**Duration**: May 2014-March 2016

**Role:** Signalling Engineer

**Description:** As a Signalling Engineer, I was responsible for

* The design and verification of the railway signaling circuit.
* Collect the inputs from the client, check with previous design styles and design the solution for the problem.
* Modifications for the design as per the client requirement.

**ACHIEVEMENTS & Co-CURRICULAR ACTIVITIES:**

* Actively Participated in organizing and received the best volunteer award for the International Conference on Emerging Technologies in Engineering-2019 in Osmania University.
* Receiving Merit Scholarship from AICTE (government of India) for pursuing higher studies by qualifying GATE.
* Selection in AM signaling company Campus Placements in 2014.
* Received certificate of Merit in an event conducted by IEEE.

**Strengths:**

* Ability to work under pressure.
* Good communication skills.
* Ability to deliver while working with a team.
* Self-Motivation.

**DECLARATION:**

I hereby declare all the above mentioned information is true to the best of my knowledge and bear the responsibility for the correction of particulars.

Date: 19-12-2019 Signature Place: Hyderabad (Thimmapoor Ashok)